

CURRICULUM VITAE



Mohamed M. Eljhani, Ph.D.

Dr. Mohamed Muftah Eljhani
Department of Computer Engineering
Faculty of Engineering
University of Tripoli

University of Tripoli: <https://uot.edu.ly/eng/ec/staff/index.php?id=5629>
Research Group: <https://uot.edu.ly/rcc/fpga/about.php?lang=ar>
Google Scholar: <https://scholar.google.com/citations?hl=en&user=Gm6R7uYAAAAJ>
LinkedIn: <https://www.linkedin.com/in/mohamed-m-eljhani-89a2853b/>
Research Gate: <https://www.researchgate.net/profile/Mohamed-Eljhani>
Academia: <https://fit.academia.edu/MohamedEljhani>

COVER LETTER

Dear All Concerned,

Experienced faculty member with a demonstrated history of working in the higher education for more than 15 years both in the academics and industries. I have published several research journal papers in the area of high-level synthesis of advanced digital systems, and digital integrated circuit design with Field-Programmable Gate Arrays (FPGA) using Hardware Description Language. Now I am teaching several computer engineering courses at University of Tripoli, and I have strong education professional with a doctor of philosophy focused in Computer Engineering from Florida Institute of Technology, a Master's of Science (M.Sc.) focused in Computer Sciences from Florida Institute of Technology, and a Master's of Science (M.Sc.) focused in Computer Engineering from Beijing University of Aeronautics & Astronautics.

More than 10 years of industry experience as a computer engineer have provided me with a broad view on problems solving that is very useful in assisting students with projects and assignments. In addition, I had more than 15 years of teaching experience as a faculty in several institutions in Libya. This experience provided me with the confidence so now I am capable of teaching a variety of computer engineering courses and supervising students in their research projects. Through this experience I have developed excellent communication, problem solving and organizational skills.

I believe that my academic training and my engineering experience prepared me to be an effective researcher and faculty member. My doctoral dissertation topic relates to designing and implementing Front-end of Wake-Up-Word Speech Recognition System (WUW-SR) on FPGA. The developed work relates to a new front-end paradigm named WUW-SR. The State-of-the-art WUW-SR enables users to activate systems (e.g., handheld devices) with speech command (e.g., Wake-Up-Word) instead of using other paradigms for activation (e.g., push to talk).

Sincerely,

CURRICULUM VITAE

KEY SKILLS

- Full time faculty member at department of Computer Engineering, University of Tripoli
- Head of study and examinations department at college of engineering, University of Tripoli
- Head of information systems and technology department at American University of Libya
- B.Eng., M.Sc., Ph.D. in Computer Engineering
- M.Sc. in Computer Science
- Experienced faculty member with more than 15 years teaching different undergraduate and graduate computer engineering courses.
- More than 10 years of industry experience as a Computer Engineer
- Published number of journal and conference research papers
- The ability to maintain high standards of achievement, behavior, discipline and punctuality amongst students

EDUCATION QUALIFICATION

- 1. Florida Institute of Technology (Melbourne, Florida)**
Doctorate of Philosophy in Computer Engineering (February 2015)
 - Emphasis on Speech Recognition, Digital Signal Processing, Embedded Systems & FPGA Design
 - Dissertation: Front-end of Wake-Up-Word Speech Recognition System Design on FPGA
 - Major GPA: 3.80/4.00
- 2. Florida Institute of Technology (Melbourne, Florida)**
Master of Science in Computer Science–Computer Information Systems (July 2014)
 - Emphasis on Computer Programming, Computer Hardware Organization, Operating Systems, and Component Engineering
 - Major GPA: 3.85/4.00
- 3. Beijing University of Aeronautics and Astronautics (Beijing, China)**
Master of Science in Computer Engineering (Dec 2004)
 - Emphasis on Complex Digital Systems Design & RISC Design
 - Thesis: RISC Design and Implementation on FPGA
 - Major GPA: 3.90/4.00

TEACHING EXPERTISE

Jan 2005 - Jun 2008 Responsible for teaching a number of courses in computer engineering at
Sep 2017 - Now University of Tripoli.

Courses taught:

- FPGA design for embedded systems with Verilog hardware description language
- Computer organization & architecture
- Microprocessor systems with assembly language
- Digital systems design

RESEARCH & PUBLICATIONS

In my Ph.D. research we designed and implemented new efficient hardware architecture for Front-end of Wake-Up-Word Speech Recognition System (WUW-SR) on FPGA. WUW-SR presents new solution by allow users to activate handheld devices with speech commands instead of “push to talk” as is presently done.

- **Front-end of Wake-Up-Word Speech Recognition System Design on FPGA**
 - Journal of Telecommunications System & Management (JTSM) - July 2013
<http://dx.doi.org/10.4172/2167-0919.1000108>
- **Wake-Up-Word Feature Extraction on FPGA**
 - World Journal of Engineering and Technology (WJET) - Feb 2014
<http://www.scirp.org/journal/WJET/>
- **Voice Activity Decoder of Wake-Up-Word Speech Recognition on FPGA**
 - International Journal of Engineering Research and Applications (IJERA) – Dec 2014
<http://www.ijera.com> Int. Journal of Engineering Research and Application ISSN: 2248-9622, Vol. 4, Issue 12(Part 3), December 2014, pp.160-168
- **Reduced Instruction Set Computer Design on FPGA**
 - 2021 IEEE 1st International Maghreb Meeting of the Conference on Sciences and Techniques of Automatic Control and Computer Engineering MI-STA – June 2021
<https://ieeexplore.ieee.org/document/9464409>
- **An Alternative Microprocessor Bus Structure Design on FPGA**
 - International Science and Technology Journal – June 2021
<https://www.stcrs.com.ly/istj/article-details.php?id=564>
- **University of Tripoli’s Real-Time Density-Based Traffic Light Controller on FPGA**
 - International Science and Technology Journal – June 2021
<https://www.stcrs.com.ly/istj/article-details.php?id=568>

- **Design and Comparative Analysis of An Alternative Approach of Bus-Interconnect for Systems on Programmable Chip**
 - International Conference on Mechanical and Industrial Engineering – Nov 2022
<https://icmie2022.ly/docs/proceedings/ICMIE-2022-37.pdf>
- **Design and Implementation of a Smart Traffic Light System with Libyan License Plate Recognition on FPGA**
 - 2023 IEEE 3rd International Maghreb Meeting of the Conference on Sciences and Techniques of Automatic Control and Computer Engineering (MI-STA) – July 2023
<https://ieeexplore.ieee.org/document/10169241>
- **Design and Implementation of Single Precision Floating-point Arithmetic Logic Unit for RISC Processor on FPGA**
 - 2023 IEEE 3rd International Maghreb Meeting of the Conference on Sciences and Techniques of Automatic Control and Computer Engineering (MI-STA) – July 2023
<https://ieeexplore.ieee.org/document/10169623>
- **Design and Implementation of Five Stages Pipelined RISC Processor on FPGA**
 - 2023 IEEE 3rd International Maghreb Meeting of the Conference on Sciences and Techniques of Automatic Control and Computer Engineering (MI-STA) – July 2023
<https://ieeexplore.ieee.org/document/10169818>
- **An Intelligent Density-Based Traffic System with Libyan Vehicles License Plate Recognition on FPGA**
 - Journal of Engineering Research (University of Tripoli) – Nov 2023
<https://www.jer.ly/PDF/Vol-36-2023/JER-07-36-Abstract.php?f=e>
- **Development and Comparative Evaluation of Innovative Bus-Interconnect Methodology for Programmable Chip-Based Systems**
 - Journal of Engineering Research (University of Tripoli) – Mar 2024
<https://jer.ly/PDF/Vol-37-2024/JER-03-37-Abstract.php?f=e>

ENGINEERING EXPERIENCE

Aug 1993 – Feb 2002 I have worked as a Computer Engineer for the Electronics Research & Development Center (Tripoli, Libya).

- Developed On-board Computer / Hardware and Software (as a team work)
- Designed and Implemented number of RISCs and Microcontrollers
- Involved in many Hardware and Software aspects of computing, from design of individual Microprocessors to Analog and Digital Circuits Design
- Wrote number of Software and Firmware for Embedded Systems

RESEARCH STATEMENT

RESEARCH INTEREST

Interested in research in embedded systems design with focus on FPGA embedded systems design, there are many challenges faced by researchers in this active field. Collaborating with researches in other disciplines to develop and enhance the existing theoretical and algorithmic frameworks is of my interest. As computer engineering and computer science graduate interested in investigating various theories and their applications to real world problems that involve; complex digital system design on FPGA, speech recognition system design, and its numerous applications that lay in the intersection of science and engineering.

BACKGROUND AND CURRENT RESEARCH

I believe that my academic training with my working experience as a computer engineer prepare me to be an effective researcher. My research work was conducted under the direction of Dr. Veton Z. Kepuska, and looks at design and implements a Front-end of Wake-Up-Word Speech Recognition System (WUW_SR) on FPGA. In our research, we developed a new front-end paradigm on FPGA that presently is not widely recognized. The state-of-the-art WUW_SR system is based on three different sets of features: Mel-frequency Cepstral Coefficients (MFCC), Linear Predictive Coding Coefficients (LPC), and Enhanced Mel-frequency Cepstral Coefficients (ENH-MFCC). These features were needed to improve the accuracy of the system by combining the individual scores. Those scores were obtained with corresponding Hidden Markov Models (HMMs) as back-end stage of the WUW-SR. Presented experimental design and implementation of a novel architecture of a real time feature extraction processor that generates those features simultaneously was the topic of my dissertation. The WUW_SR is designed and implemented in Altera DSP development kit with Cyclone III FPGA as a portable system acting as a processor that is capable of computing three different sets of features at very fast rate. It is cost effective solution that consumes very little power, and it is not limited by having to operate on a general-purpose computer. This solution hence, is suitable to be used on any portable device. As a faculty member I want to incorporate my research into teaching to enhance the existing courses and to develop new classes. I believe combining research and teaching will improve both. In my Ph.D. research, the efficient hardware architecture and implementation of front-end of WUW-SR has been presented. We have described relevant parts of front-end from theory to hardware design. These designs were implemented on FPGA, and the results of these implementations which included comparing them with software equivalents have been presented in number of publications that were listed above. The computational complexity and memory requirement of these sets of feature algorithms is analyzed in detail. The presented solution is very efficient as disused in papers. The most important characteristic of a WUW-SR system is that it should guarantee virtually 100% correct rejection of non-WUW out of vocabulary words while maintaining correct acceptance rate of 99.9% or higher for in vocabulary words.

STATEMENT OF TEACHING PHILOSOPHY

OVERVIEW

Teaching and advising students are about making students to continue to love learning, which is one of my primary motivations for pursuing an academic career. My background and experience provide the expertise needed for teaching courses at both graduate and undergraduate level. Furthermore, helping students to establish their own research goals and pursue lifelong learning is of special interest to me. I have mentored undergraduate students from different universities in the past. They varied in background, motivation, initial skillfulness, and personality and I enjoyed finding the best way to guide each individual student to achieve his/her individual academic goals. I started my carrier as instructor on January 2005 as a faculty of the University of Tripoli where I was engaged as instructor in several computer engineering courses. Academia provides an opportunity to combine research and teaching in a way not possible in any other setting. I have been fortunate to teach students there, including the development of new FPGA design course. I believe that an important characteristic of effective teaching at the college level is the use of research results to enhance the student learning experience.

Computer engineering is my primary research area. My focus is on researching useful teaching techniques to advising and curriculum development for improving my content delivery. I will use this experience to continuously work on improving my teaching approach. Computer engineering is a collection of theoretical studies, experimental methods, and engineering design. My teaching philosophy focuses on giving students the first-hand experience on all three aspects, with an emphasis on engineering design. Besides learning fundamentals from lectures, students will continue improving their skills through designing, implementing, and experimenting with their own systems to solve real-world problems. In particular, I usually encourage students to start from implementation of an existing approach and finish with new design and implementation approach. Once the student has gained confidence and familiarity on the problem, with guidance, they can identify the limitation of the existing approaches and develop new ideas to improve them. In classroom, practical experience is also the key to deliver new knowledge. Another important part of my teaching philosophy is to encourage students to develop their communication skills. I encourage students to collaborate with each other in group projects. They are also encouraged to communicate with me through all means: e-mail, phone, office hours, etc. In my experience, communication is the key to a student's success. Finally, I encourage students to think widely about the problem and apply their accumulated knowledge to solving it. Computer engineering is a fast moving and ever-changing discipline. A student's vision should not be limited to a specific approach. I feel strongly that students should understand the big picture; identifying what is the key issue of the problem that they are trying to solve, what kind of research is required, experiment with various approaches, and picking the most suitable solution, not necessarily the best solution.

CONTACT REFERENCES

1. **Dr. Veton Z. Kepuska, Associate Professor, Academic Advisor.**
Electrical & Computer Engineering Department
Florida Institute of Technology
Olin Engineering Building
150 West University Blvd.
Melbourne, FL 32901-6975
vkepuska@fit.edu
Tel. (321) 674-7183
2. **Dr. Samuel Kozaitis, Professor and ECE Department Head, serving on my doctoral committee.**
Electrical & Computer Engineering Department
Florida Institute of Technology
Olin Engineering Building
150 West University Blvd.
Melbourne, FL 32901-6975
kozaitis@fit.edu
Tel. (321) 674-8060
3. **Dr. Rhoda Baggs, Assistant Professor and Computer Information Systems Program Chair.**
Department of Computer Sciences
Florida Institute of Technology
150 West University Blvd.
Melbourne, FL 32901
rbaggs@fit.edu
Tel.(321) 674-8807
4. **Dr. Marius Silaghi, Associate Professor, serving on my doctoral committee**
Department of Computer Sciences
Florida Institute of Technology
504LSA, 207
150 West University Blvd.
Melbourne, FL 32901
msilaghi@cs.fit.edu
Tel. (321) 674-7493